



Board Setup For SIS Receivers

- a) R8, R9, R29 shorted
- b) H28=15K, H20=1K
- c) ISense: X1 Open, R4=68, X2 Shorted, (G=240 to 600, 500 Nom.)
- d) VSense: X11 Shorted, X13 Shorted, (G=57 to 200, 100 Nom.)
- e) R6, R7, R26 and R27 Left Open for improved CMMR
- f) To minimize resistance of R1 and R21, turn fully clockwise
- g) C1 - C4 are 1000pF Ceramic (Suppresses computer noise)
- h) R1 = 100 Trimmer, R21 = 500 Trimmer
- i) C30 and C31 are located on the Connector itself

Date	REVISIONS	Reason	FATCO
			THE DEWAR MAKERS
			IF WE DON'T MAKE IT, YOU DON'T NEED IT.
	TITLE		
	SIS COMPUTER SIS PREAMP CIRCUIT MARK III		
	DWG. NO.		SHEET 1 of 1
	ISSUED	12/Apr/93	
	DRAWN by	PLS	

L1 - L4 are 1mH Toroid Coils, High μ , ID=50, OD=350, 36Ga. WW Wire @ 25 Turns
 L5 - L8 are 11mH Toroid Coils, High μ , ID=100, OD=250, 36Ga. WW Wire @ 12 Turns
 L1 thru L8 are used only in a hi EMI environment for noise suppression
 Preamp Circuit is Floating with respect to Dewar Gnd.

NOTE:
 Pins G and T are Power Gnd and are not used with this configuration.
 The reason is noise suppression. THE ONLY Power Gnd Returns are
 SIS1 BIAS RETURN and SIS2 BIAS RETURN (Pins B and M).

Note: Dewar ground must float with respect to GROUND (G & T).
 SIS1 BIAS RETURN and SIS2 BIAS RETURN. This is critical to noise control.
 Do not connect preamp ground to box or Dewar.